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Substitute for form 1449A/PTO				*Complete if Known*	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				**Application Number**	unassigned
				Filing Date	September 28, 2001
				First Named Inventor	John D. Acton
				Group Art Unit	unassigned
(use as many sheets as necessary)				Examiner Name	unassigned
Sheet	1	of	2	Attorney Docket Number	P6196 US

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	U.S. Patent Document Number	Kind Code[2] (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
SMB	A	5,375,127		Leak et al.	Dec. 20, 1994	
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Examiner Initials*		Cite No.[1]	Foreign Patent Document Office[3]	Number[4]	Kind Code[5] (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]

Stephen M Becker 5/28/04

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Substitute for form 1449B/PTO				*Complete if Known*	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				**Application Number**	unassigned
				Filing Date	September 28, 2001
				First Named Inventor	John D. Acton
				Group Art Unit	unassigned
(use as many sheets as necessary)				Examiner Name	unassigned
Sheet	2	of	2	Attorney Docket Number	P6196 US

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume–issue number(s), publisher, city and/or country where published.	T[2]
SLB	F	HEGDE, ANUPAMA, "Error Detection and Correction with IDT49C466," *Integrated Device Technology, Inc., Appl. Note AN–94*, 1996, pages 131–141.	
SLB	G	Supplement A.6–5, "Error Detection and Correction," *Logic and Computer Design Fundamentals*, 1997, 2000 Prentic–Hall, Inc., pages 1–5.	
SLB	H	WICKMAN ET AL., "Cost Models for Large File Memory DRAMs with ECC and Bad Block Marking," *Department of Electrical and Computer Engineering, University of Alberta, Edmonton, AB T6G 2G7, CANADA*, pages 1–9.	

Examiner Signature	Stephen M Baker	Date Considered	5/28/04

(1449B/PTO Modified)

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